# SiPearl & Arm will be present at the Open Innovation Platform® Ecosystem Forum organised by TSMC on Wednesday 26 August

SiPearl, the company that is designing the high-performance, low-power microprocessor for the European exascale[1] supercomputer, will be taking part in the Open Innovation Platform® Ecosystem Forum organised on 26 August by the Taiwanese group TSMC, the world's leading independent semiconductor foundry. For this online event, SiPearl will be working alongside Arm, the global semiconductor technology supplier, to host a conference on "Accelerating HPC market adoption with Arm Neoverse POP™ IP".

**Maisons-Laffitte, France, 30 July 2020** – SiPearl, the company that is designing the high-performance, low-power microprocessor for the European exascale supercomputer, will be a guest speaker at the upcoming Open Innovation Platform® Ecosystem Forum, organised by its industrial partner TSMC, the world's leading independent semiconductor foundry.

During this annual event, which brings together stakeholders from the ecosystem created by TSMC, Ying-Chih Yang, SiPearl's Chief Technical Officer, Craig Prunty, SiPearl's Vice President Marketing and Business Development, and Dr Selma Laabidi, Arm Product Expert, will lead a conference on "Accelerating HPC market adoption with Arm Neoverse POP™ IP".

In April this year, SiPearl announced that it had signed a major licensing agreement with ARM for the development of its first generation of microprocessors. Under the terms of this agreement, SiPearl will benefit from the high-performance, secure and scalable next-generation Arm® Neoverse™ platform, codenamed **"Zeus"**, as well as Arm's robust software and hardware ecosystem. The "Zeus" platform, which includes Arm's POP™ intellectual property on advanced FinFET[2] technology, will enable SiPearl to accelerate its design and ensure outstanding reliability for a very high-end offering, in terms of both computing power and energy efficiency.

## About SiPearl

Created by Philippe Notton, SiPearl is the Franco-German company that is bringing to life the European Processor Initiative (EPI) project, designing the high-performance, low-power microprocessor for the European exascale supercomputer.

This new generation of microprocessors will enable Europe to set out its technological sovereignty on the strategic markets for high performance computing, artificial intelligence and connected mobility.

SiPearl is developing and will market its solutions through close collaboration with its 26 partners from the EPI - scientific community, supercomputing centres and leading names from the IT, electronics and automotive industries - which are its stakeholders and future clients. It is supported by the European Union[3].

SiPearl is also a member of the Mont-Blanc 2020 consortium to equip Europe with a dedicated modular and energy-efficient high performance computing microprocessor, and is a member of the PlayFrance.Digital collective for Europe to lead the field for digital technology.

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[1] *One billion billion calculations per second.*

[2] *FinFET is the current trend for semiconductors to build a 3D transistor with a fin-shaped structure.*

[3] *This project has received funding from the European Union's Horizon 2020 research and innovation programme under specific grant agreement no.826647.*