

Ying-Chih Yang joins SiPearl as CTO

SiPearl, the company that is bringing to life the European Processor Initiative (EPI) consortium's project by designing the microprocessor for the European exascale¹ supercomputer, is announcing that it has appointed Ying-Chih Yang as Chief Technical Officer. As the microprocessor's lead architect, he will head up all its developments.

Maisons-Laffitte, France, 9 March 2020 – Ying-Chih Yang (49, Master's in Electronic Engineering from National Chiao-Tung University in Taiwan) is joining SiPearl's leadership team as Chief Technical Officer.

With his deep expertise in complex system-on-chip development, Ying-Chih Yang will be the lead architect for the high-performance, low-power microprocessor developed by SiPearl within the EPI consortium to secure Europe's technological sovereignty and independence on the strategic markets for high performance computing, artificial intelligence and connected mobility.

Ying-Chih Yang has a dual French-Taiwanese background and has an extensive track record working closely with Philippe Notton, SiPearl's founder. Together, they have successfully driven several projects: with MStar Semiconductor, a Taiwan-based startup specialised in set-top boxes for pay-television, then STMicroelectronics' Consumer division and Atos for the European microprocessor project led by the EPI consortium.

Before joining SiPearl, Ying-Chih Yang was Global Architect with Atos from July 2018, in charge of the design of the high-performance architecture for the future European microprocessor. Previously, he was Advanced Systems and Advanced Security Chief Officer for STMicroelectronics' Consumer division.

The appointment of other renowned experts to join SiPearl's leadership team will be officially announced during the first half of this year.

About SiPearl

Created by Philippe Notton, SiPearl is the company that is bringing to life the European Processor Initiative (EPI) project, designing the high-performance, low-power microprocessor for the European exascale¹ supercomputer.

This new generation of microprocessors will enable Europe to set out its technological sovereignty on the strategic markets for high performance computing, artificial intelligence and connected mobility.



SiPearl will develop and market its solutions through close collaboration with its 26 partners from the EPI³ - scientific community, supercomputing centres and leading names from the IT, electronics and automotive sectors - which are its stakeholders and future clients.

SiPearl is supported by the European Union².

Media contact:

Mag and Co: Marie-Anne Garigue – +33 (0)6 09 05 87 80 – sipearl@mag-and-co.fr

¹ 1 billion billion calculations per second.

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