

## SiPearl Joins the CXL™ Consortium Behind Compute Express Link™, the Breakthrough CPU-to-Device Interconnect

SiPearl, the company that is designing the high-performance, low-power microprocessor for the European exascale<sup>1</sup> supercomputer, has joined the CXL™ Consortium founded by the world leaders Alibaba, Cisco, Dell EMC, Facebook, Google, Hewlett Packard Enterprise, Huawei, Intel Corporation and Microsoft. They are behind Compute Express Link™ (CXL), the new high-bandwidth, low-latency interconnect protocol between microprocessors and devices that leverages the PCI Express® (PCIe®) 5.0 physical layer infrastructure.

**Maisons-Laffitte (France) and Duisburg (Germany), October 22, 2020** – SiPearl, the company that is designing the high-performance, low-power microprocessor for the European exascale supercomputer, has joined the CXL Consortium, whose founding members are the world leaders Alibaba, Cisco, Dell EMC, Facebook, Google, Hewlett Packard Enterprise, Huawei, Intel Corporation and Microsoft. Together, they are developing the technical specifications for the CXL technology, which will be the future open high-performance protocol for interconnecting microprocessors, their devices (graphics processors, accelerators, etc.) and remote machines.

Based on the physical and electrical interface from PCI Express 5.0, CXL will enable better resource sharing between the processor's memory space and memory on attached devices to deliver higher performance, a unified memory space, reduced software stack complexity and lower overall system costs, while ensuring compatibility with existing standards.

*"Our membership of the CXL Consortium will help ensure excellence for our future clients, including major contracting authorities for high performance computing, by offering them a platform that is open to the major future standards for their industries",* explains Philippe Notton, SiPearl's CEO and founder.

*"We are supporting SiPearl with its membership of the CXL™ consortium and its strategy to position itself on the most open high-performance technologies. The close integration of its microprocessors with the best accelerators to arrive on the market will be a major innovation for exascale class supercomputers and beyond, offering more simplicity and performance, while remaining open and interoperable",* adds Jacques-Charles Lafoucrière, CEA DAM's HPC Program Manager.

*"We are pleased to have SiPearl join other industry leaders in the CXL Consortium"* concludes Jim Pappas, CXL Consortium Chairman. *"We look forward to SiPearl's contribution to help accelerate the adoption of the CXL industry standard and growth of the CXL ecosystem."*



## About SiPearl

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Created by Philippe Notton, SiPearl is the Franco-German company that is bringing to life the European Processor Initiative (EPI) project, designing the high-performance, low-power microprocessor for the European exascale supercomputer.

This new generation of microprocessors will enable Europe to set out its technological sovereignty on the strategic markets for high performance computing, artificial intelligence and connected mobility.

SiPearl is developing and will market its solutions through close collaboration with its 26 partners from the EPI - scientific community, supercomputing centres and leading names from the IT, electronics and automotive industries - which are its stakeholders and future clients. It is supported by the European Union<sup>2</sup>.

SiPearl is also a member of the Mont-Blanc 2020 consortium to equip Europe with a dedicated modular and energy-efficient high performance computing microprocessor, and is a member of the PlayFrance.Digital collective for Europe to lead the field for digital technology.

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<sup>1</sup> One billion billion calculations per second.

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